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buffers ports switching-planes "linked lists"

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[Network link access scheduling using a plurality of prioritized lists containing queue identifiers - all 4 versions »](#)

SA Hauser, RG Bubenik, SA Caldara, ME Gaddis, TA ... - US Patent 5,996,019, 1999 - freepatentsonline.com
... system with interrogation of output port servers for ... apparatus for local memory buffers management for ... Method for handling redundant switching planes in packet ...

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[A Terabit Multiservice Switch - all 7 versions »](#)

KY Yun - 2001 - doi.ieeecomputersociety.org
... with input buffer x and output buffer y. Cyclone ... up to 32) between input ports and output ports. ... of the algorithm run on multiple switching planes (one for ...

[Cited by 20 - Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Multicasting facilities in ATM switching architectures. A study of several approaches](#)

J Garcia-Haro, J Malgosa-Sanahuja, JL Melus-Moreno - Communications, Computers, and Signal Processing, 1995, ..., 1995 - ieeexplore.ieee.org
... is considering that the system consists of several (S) homogeneous switching planes working in ... G input ports share a common input buffer module able to ...

[Cited by 1 - Related Articles](#) - [Web Search](#)

[Multipoint-to-point arbitration in a network switch - all 4 versions »](#)

TA Manning, SA Caldara, SA Hauser, ML Colsman - US Patent 5,982,776, 1999 - freepatentsonline.com
... system with interrogation of output port servers for ... apparatus for local memory buffers management for ... Method for handling redundant switching planes in packet ...

[Cited by 7 - Related Articles](#) - [Cached](#) - [Web Search](#)

[ATM Switch Chipset using Serial Link and Routing Technology](#)

SGSTM Limited - Embedded Microprocessor Systems, 1996 - books.google.com
... can be connected through the redundant switching planes to all ... to right, including the additional ports for the ... shown, via which free cell buffer addresses are ...

[Related Articles](#) - [Web Search](#)

[Distributed control switching network for multi-line telephone communications - all 3 versions »](#)

JM Cotton, NC Olsen, AT Wissink, GV Pieper, WA ... - US Patent 5,255,264, 1993 - freepatentsonline.com
... four section switches on four of the six 33 switching planes. ... crosspoint memory locations and the switch ports are accessed ... 3a through the four FIFO buffers. ...

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[Point-to-multipoint transmission using subqueues - all 5 versions »](#)

SA Caldara, SA Hauser, TA Manning - US Patent 6,141,346, 2000 - freepatentsonline.com
... system with interrogation of output port servers for ... apparatus for local memory buffers management for ... Method for handling redundant switching planes in packet ...

[Cited by 7 - Related Articles](#) - [Cached](#) - [Web Search](#)

[Apparatus and method for large scale ATM switching - all 2 versions »](#)

US Patent 5,303,078, 1994 - freepatentsonline.com
... illustrating the staggered operation of the "m" switching planes and the ... Also preferably, there is a one-cell buffer at each of the m input ports 60 used ...

[Cited by 48 - Related Articles](#) - [Cached](#) - [Web Search](#)

Frame classification using classification keys - all 3 versions »

SA Hauser, JL Williams - US Patent 5,748,905, 1998 - freepatentsonline.com

... system with interrogation of output port servers for ... apparatus for local memory buffers management for ... Method for handling redundant switching planes in packet ...

[Cited by 29 - Related Articles](#) - [Cached](#) - [Web Search](#)Channel allocation system for distributed digital switching network - all 2 versions »

JM Cotton, GV Pieper - US Patent 5,623,489, 1997 - freepatentsonline.com

... four section switches on four of the six switching planes. ... Therefore, the call message FIFO buffers allow interaction ... there can be multiple ports and channels ...

[Cited by 14 - Related Articles](#) - [Cached](#) - [Web Search](#)Broadcast system for distributed switching network - all 2 versions »

JM Cotton, NC Olsen, N Necula, WA Oswald - US Patent 5,237,571, 1993 - freepatentsonline.com

... to four section switches on four of the six switching planes. ... processor section 146, the link data ports 148 of ... section via the message FIFO buffers 156, 158 ...

[Cited by 8 - Related Articles](#) - [Cached](#) - [Web Search](#)Redundant switch system and method of operation - all 4 versions »

TA Manning, SA Caldara, SA Hauser - US Patent 5,909,427, 1999 - freepatentsonline.com

... system with interrogation of output port servers for ... apparatus for local memory buffers management for ... Method for handling redundant switching planes in packet ...

[Cited by 13 - Related Articles](#) - [Cached](#) - [Web Search](#)Conferencing system for distributed switching network - all 3 versions »

JM Cotton, NC Olsen, AT Wissink, GV Pieper, WA ... - US Patent 5,379,280, 1995 - freepatentsonline.com

... four section switches on four of the six switching planes. ... Therefore, the call message FIFO buffers allow interaction ... there can be multiple ports and channels ...

[Cited by 8 - Related Articles](#) - [Cached](#) - [Web Search](#)Asynchronous transfer mode based service consolidation switch

RB McClure, TA Manning, SA Caldara, SA Hauser - US Patent 5,850,395, 1998 - patentsform.us

... system with interrogation of output port servers for ... apparatus for local memory buffers management for ... Method for handling redundant switching planes in packet ...

[Related Articles](#) - [Cached](#) - [Web Search](#)Allocated and dynamic switch flow control - all 8 versions »

SA Caldara, SA Hauser, TA Manning - US Patent 5,978,359, 1999 - freepatentsonline.com

... system with interrogation of output port servers for ... apparatus for local memory buffers management for ... Method for handling redundant switching planes in packet ...

[Cited by 15 - Related Articles](#) - [Cached](#) - [Web Search](#)Technique for resolving output port contention in a high speed packet switch - all 3 versions »

US Patent 5,157,654, 1992 - freepatentsonline.com

... from input buffer 340 into output buffer 350 and ... number of input or output ports without necessarily ... of the input modules and the switching planes and between ...

[Cited by 45 - Related Articles](#) - [Cached](#) - [Web Search](#)Method and apparatus for discarding frames in a communications device - all 7 versions »

SA Caldara, SA Hauser, TA Manning, DN Peck - US Patent 5,822,540, 1998 - freepatentsonline.com

... system with interrogation of output port servers for ... apparatus for local memory buffers management for ... Method for handling redundant switching planes in packet ...

[Cited by 9 - Related Articles](#) - [Cached](#) - [Web Search](#)Broadcast system for distributed controlled switching network - all 3 versions »

JM Cotton, NC Olsen, N Necula, WA Oswald - EP Patent 0,534,785, 2002 - freepatentsonline.com

... to four section switches on four of the six switching planes. ... processor section 146, the link data ports 148 of ... section via the message FIFO buffers 156, 158 ...

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Distributed controlled switching network for telephone communications - all 3 versions »

JM Cotton, NC Olsen, AT Wissink, GV Pieper, N ... - EP Patent 0,534,784, 2001 - freepatentsonline.com
... to four section switches on four of the six switching planes. ... processor section 146,
the link data ports 148 of ... section via the message FIFO buffers 156, 158 ...

Cited by 1 - Related Articles - Cached - Web Search

Network switch utilizing centralized and partitioned memory for connection topology

information ... - all 5 versions »

TA Manning, SA Caldara, SA Hauser, AD Sherman - US Patent 5,917,805, 1999 - freepatentsonline.com
... system with interrogation of output port servers for ... apparatus for local memory buffers
management for ... Method for handling redundant switching planes in packet ...

Cited by 5 - Related Articles - Cached - Web Search

Method and apparatus for providing buffer state flow control at the link level in addition to flow

... - all 7 versions »

TA Manning, SA Caldara, SA Hauser, DH Hunt, RL ... - US Patent 6,256,674, 2001 - freepatentsonline.com
... system with interrogation of output port servers for ... apparatus for local memory buffers
management for ... Method for handling redundant switching planes in packet ...

Cited by 8 - Related Articles - Cached - Web Search

Link buffer sharing method and apparatus - all 5 versions »

TA Manning, SA Hauser, SA Caldara, RL Strouble, DH ... - US Patent 5,781,533, 1998 - freepatentsonline.com
... system with interrogation of output port servers for ... apparatus for local memory buffers
management for ... Method for handling redundant switching planes in packet ...

Cited by 5 - Related Articles - Cached - Web Search

Packet address look-ahead technique for use in implementing a high speed packet switch - all

2 versions »

A Cisneros, GA Hayward, IP Auer - US Patent 5,166,926, 1992 - freepatentsonline.com
... routing header, to the specific output port to which ... each one of the cross-point
switching planes with each ... with a shared memory containing a buffer having a ...

Cited by 45 - Related Articles - Cached - Web Search

Method and apparatus for reducing information loss in a communications network - all 4

versions »

RB McClure, SA Caldara, SA Hauser, TA Manning - US Patent 5,790,770, 1998 - freepatentsonline.com
... system with interrogation of output port servers for ... apparatus for local memory buffers
management for ... Method for handling redundant switching planes in packet ...

Cited by 24 - Related Articles - Cached - Web Search

Large fault tolerant packet switch particularly suited for asynchronous transfer mode (ATM) ...

- all 2 versions »

A Cisneros - US Patent 5,130,984, 1992 - freepatentsonline.com
... H. Kuwahara et al, "A Shared Buffer Memory Switch ... Switching planes 550 are formed
of multiple planes of self ... appropriate cells from the input ports, which are ...

Cited by 47 - Related Articles - Cached - Web Search

Joint flow control mechanism in a telecommunications network - all 8 versions »

TA Manning, SA Caldara, SA Hauser, AD Sherman - US Patent 6,088,736, 2000 - freepatentsonline.com
... system with interrogation of output port servers for ... apparatus for local memory buffers
management for ... Method for handling redundant switching planes in packet ...

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